

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Shrenik Deliwala

Serial No.: Not yet known

Filed: February 20, 2002

For: INTEGRATED OPTICAL/ELECTRONIC  
CIRCUITS AND ASSOCIATED  
METHODS OF SIMULTANEOUS  
GENERATION THEREOF

: Attorney Docket No.: 53168-500301D6

#3  
5/30/02  
m. Pruden

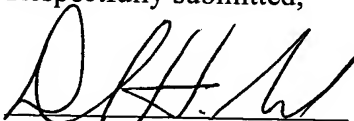
LETTER TO OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents  
Washington, D.C. 20231

Pursuant to M.P.E.P. § 608.02, enclosed please find the proposed changes to figures 5, 13, 20, 21, 22, 30, 47, 51, 52, 59, 60, 68A, 68B, 68C, 68D, 73, 74, 75, 76, 77, 78, 82, 83 and 88. No new matter has been added by way of this amendment.

If there are any remaining informalities with the drawings, Applicant hereby requests issuance of a Form PTO-948 identifying the informalities noted by the Official Draftsperson.

Respectfully submitted,



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Dated: February 20, 2002

EXPRESS MAIL CERTIFICATE (37 C.F.R. § 1.10)

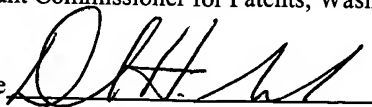
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Date of Deposit February 20, 2002

I hereby certify that this paper, and the papers and/or fees referred to herein as transmitted, submitted or enclosed, are being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Name Daniel H. Golub

Signature



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